

**REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.	10/711,959	Conf. No.	5958
In Re Application of:	Heng et al.	Art Unit:	2825
Filed:	10/15/2004	Dkt. #:	BUR920040201US1 (IBMB-0059)
Title:	INTEGRATED CIRCUIT SELECTIVE SCALING	Examiner:	Kik, Phallaka

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

*Entered
per
PCE
4/5/07
PB*

AFTER-FINAL AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS

In response to the Final Office Action of January 05, 2007, please amend the above-referenced patent application as follows: